

Rockwell Automation Application Content

Machine Builder Libraries



Reference Manual

Home – CIP Drive

raM_Opr_Home_CD

v2.x

Important User Information

Solid-state equipment has operational characteristics differing from those of electromechanical equipment. Safety Guidelines for the Application, Installation and Maintenance of Solid State Controls (publication SGI-1.1 available from your local Rockwell Automation sales office or online at <http://literature.rockwellautomation.com>) describes some important differences between solid-state equipment and hard-wired electromechanical devices. Because of this difference, and because of the wide variety of uses for solid-state equipment, all persons responsible for applying this equipment must satisfy themselves that each intended application of this equipment is acceptable.

In no event will Rockwell Automation, Inc. be responsible or liable for indirect or consequential damages resulting from the use or application of this equipment.

The examples and diagrams in this manual are included solely for illustrative purposes. Because of the many variables and requirements associated with any particular installation, Rockwell Automation, Inc. cannot assume responsibility or liability for actual use based on the examples and diagrams.

No patent liability is assumed by Rockwell Automation, Inc. with respect to use of information, circuits, equipment, or software described in this manual.

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Throughout this manual, when necessary, we use notes to make you aware of safety considerations.

WARNING



Identifies information about practices or circumstances that can cause an explosion in a hazardous environment, which may lead to personal injury or death, property damage, or economic loss.

IMPORTANT

Identifies information that is critical for successful application and understanding of the product.

ATTENTION



Identifies information about practices or circumstances or death, property damage, or economic loss. Attentions avoid a hazard, and recognize the consequence.

SHOCK HAZARD



Labels may be on or inside the equipment, that dangerous voltage may be present.

BURN HAZARD



Labels may be on or inside the equipment, for example, a drive or motor, to alert people that surfaces may reach dangerous temperatures.

Table of Contents

Table of Contents 3

1 Overview..... 4

 1.1 Prerequisites 4

 1.2 Functional Description 5

 1.3 Execution..... 6

2 Instruction..... 8

 2.1 Footprint 8

 2.2 Input Data 8

 2.3 Output Data 8

 2.4 Error Codes..... 8

3 Application Code Manager 10

 3.1 Definition Object: raM_Opr_Home_CD 10

 3.2 Implement Object: raM_LD_Home_CD..... 10

 3.3 Attachments..... 10

4 Application..... 11

5 Appendix..... 12

 General..... 12

 Common Information for All Instructions..... 12

 Conventions and Related Terms..... 12

1 Overview

raM_Opr_Home_CD:

- Home a CIP axis as configured in the Axis Properties.
- Method doesn't sync back path axis to physical, use raM_Opr_SyncPthPhyAx_CD for that purpose after homing is finished (either path to physical or physical to path)

Use when:

- Using a Axis Handler for Axis Management
- Homing is desired

Do NOT use when:

- Not Using a Axis Handler

1.1 Prerequisites

- Axis Handler for Axis Management
- Studio 5000 - Logix Designer
 - v30.0 →
- Studio 5000 - Application Code Manager
 - V4.0 →

1.2 Functional Description

Home object provides simplified functionality of an MAH instruction with advanced diagnostic and virtualization support from the axis handler.



General Status Bit Behavior:

Note: Status bit not shown on the output side of the instruction are not used and will not exist in the instruction backing tag.

Status Bit	Description / Behavior
*.Sts_EO	<ul style="list-style-type: none"> Enable Out indicated the status of the output line of the instruction. If false (logically LO) any instruction on the ladder rung between the instruction and the neutral rail will not be energized. If the instruction is removed from ladder scan either in a conditional subroutine, MCR zone, JMP/LBL etc., the bit will remain in its last evaluated state.
*.Sts_EN	<ul style="list-style-type: none"> The rung-in condition of the ladder rung is true and the instruction is being evaluated. If the instruction is removed from ladder scan either in a conditional subroutine, MCR zone, JMP/LBL etc., the bit will remain in its last evaluated state.
*.Sts_ER	<ul style="list-style-type: none"> If the instruction experiences an internal error, the *. Sts_ER bit will be set. Error codes / Extended codes can be found by monitoring the backing tag *.Sts_ERR / *.Sts_EXERR members respectively. If the instruction is removed from ladder scan either in a conditional subroutine, MCR zone, JMP/LBL etc., the bit will remain in its last evaluated state.
*.Sts_DN	<ul style="list-style-type: none"> Used when the execution of the instruction completes within a single scan. If the instruction is removed from ladder scan either in a conditional subroutine, MCR zone, JMP/LBL etc., the bit will remain in its last evaluated state.
*.Sts_IP	<ul style="list-style-type: none"> Used to identify the instruction is in the process If the instruction is removed from ladder scan either in a conditional subroutine, MCR zone, JMP/LBL etc., the bit will remain in its last evaluated state.
*.Sts_PC	<ul style="list-style-type: none"> Used when the execution of the instruction requires more than a single scan to complete, and indicates the 'process' carried out by the instruction has successfully completed. If the instruction is removed from ladder scan either in a conditional subroutine, MCR zone, JMP/LBL etc., the bit will remain in its last evaluated state.

1.3 Execution

- Level

1.3.1 Overview

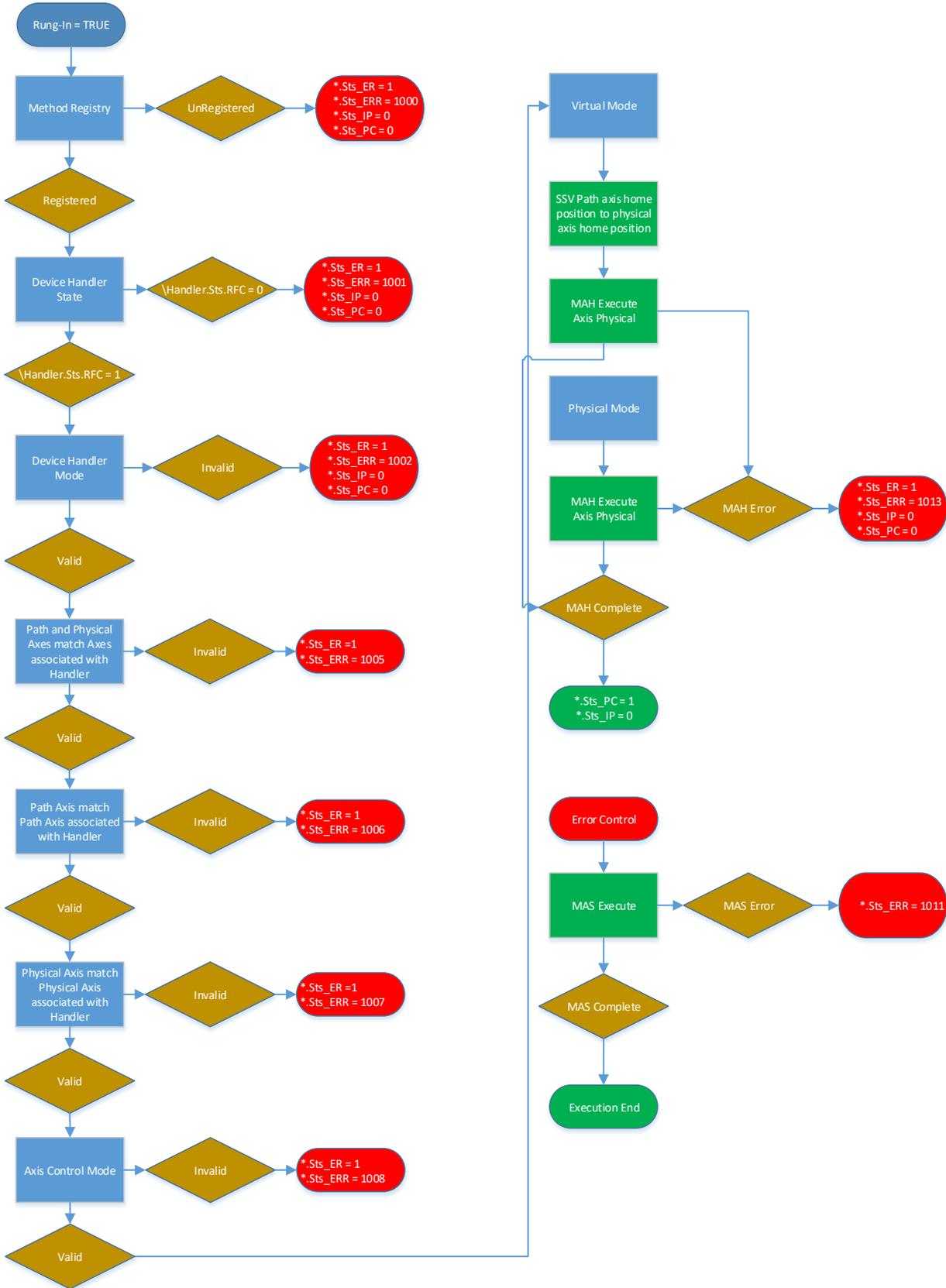
Rung in condition transition response:

- False → True
 - *.Sts_EO = 1
 - *.Sts_EN = 1
 - *.Sts_ER = 0
 - *.Sts_IP = 1
 - IF: Axis Home is Completed
 - THEN: *.Sts_IP = 0 and Sts_PC = 1
 - IF: Axis Home is in Error
 - THEN: *.Sts_ER = 1 and *.Sts_IP = 0 and *.Sts_PC = 0
- True → False
 - *.Sts_EO = 0
 - *.Sts_EN = 0
 - *.Sts_IP = 0
 - *.Sts_PC = 0
 - IF: Axis Home is in Error
 - THEN: *.Sts_ER = 1

1.3.2 Affected Axis Handler Status

Status	Value
{TagName}_Sts.Referenced	1

1.3.3 Execution Table



2 Instruction

2.1 Footprint

Characteristic	Description	Value	Unit
Definition	Estimated memory required to store the object definition, including all dependents with exception of components already installed by Axis Handler.	17.7	kB
Instance	Estimated memory required per object instantiated. This includes the object instance and all datatypes required to verify the project. In the case of user configurable arrays, an application relevant array length will be used for estimation.	0.7	kB
Execution L7x	Estimated execution time / scan footprint evaluated in 1756-L7x PAC	330	us

2.2 Input Data

Input	Function / Description	DataType
Ref_AxisPth	Path Axis	AXIS_VIRTUAL
Ref_AxisPhy	Physical axis	AXIS_CIP_DRIVE
Ref_Handle	Axis Handler Data Structure	raM_UDT_Dvc_xADH_DataHndl
Cfg_Timeout	Timeout seconds for homing. Enter 0 to disable.	DINT

2.3 Output Data

Output	Function / Description	DataType
raM_Opr_Home_CD	Object Identifier	BOOL
Sts_ERR	Instruction Error Code	DINT
Sts_EXERR	Instruction Extended Error Code	DINT
Sts_MtdID	Method ID Number	DINT

2.4 Error Codes

Sts_ERR	Description
0	No error present
1000	Method failed to register. Method will not execute until registered. Method Registry Array must be larger
1001	Axis Handler is not in a running state. Commands to the device cannot be processed.
1002	Axis Handler is not in a supported mode.
1005	Path and Physical Axes referenced by instruction do not match Axes associated with the Handler.
1006	Path Axis referenced by instruction does not match Path Axis associated with the Handler.
1007	Physical Axis referenced by instruction does not match Physical Axis associated with the Handler.
1008	Invalid Axis Control Mode. Axis must be configured for Velocity or Position.
1011	MAS Instruction error (please see EXERR for internal MAS error)
1013	MAH Instruction error (please see EXERR for internal MAM error)
1014	MAH interrupted externally
1015	Home timeout. Homing time exceeded time set in Cfg_Timeout.

Sts_EXERR	Description
< Number >	If a native instruction error occurs internally, the value of the instruction *.ERR DINT will be placed in Sts_EXERR.

3 Application Code Manager

3.1 Definition Object: raM Opr Home CD

This object contains the AOI definition and used as linked library to implement object. This gives flexibility to choose to instantiate only definition and create custom implement code. User may also create their own implement library and link with this definition library object.

3.2 Implement Object: raM LD Home CD

Implement Language: Ladder Diagram

Parameter Name	Default Value	Instance Name	Definition	Description
ObjectName	raM_Opr_Home_CD			Object Name
RoutineName	{ObjectName}	{RoutineName}	Routine	Name of the routine where the object will be placed
TagName	_ {ObjectName}	{TagName}	Local Tag	Instruction Backing Tag
StartBitTagName	Cmd_Start{ObjectName}		Local Tag	Start bit Tag Name

Linked Library

Link Name	Catalog Number	Revision	Solution	Category
raM_LD_AxisHandler_CD	raM_LD_AxisHandler_CD	>=2.0	(RA-LIB) Machine	DvcHdlr – CIP Motion
raM_Opr_Home_CD	raM_Opr_Home_CD	>=2.0	(RA-LIB) Machine	General Motion

Interface

Interface Name	Linked Library	Revision
MethodInterface	raM_LD_AxisHandler_CD	1.0

MethodInterface Members

Member Name	Description
AHPrgName	Program name where Axis Handler resides
AxisName_PHY	Name of Physical Axis attached to Handler
AxisName_PTH	Name of Path Axis attached to Handler
AHTagName	Handle Tag name of Axis Handler

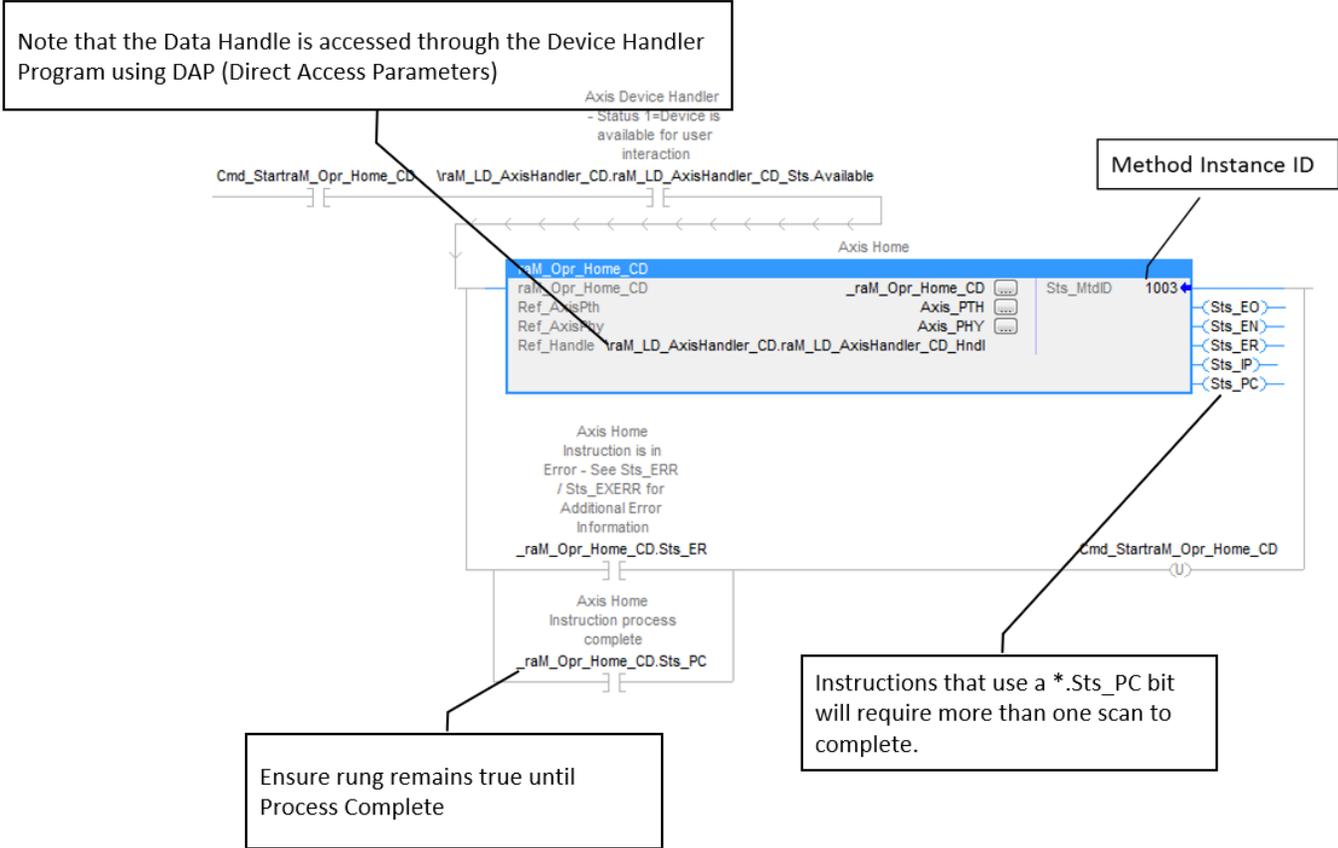
3.3 Attachments

Name	Description	File Name	Extraction path
V2_{LibraryName}	Reference Manual	RM-_{LibraryName}.pdf	{ProjectName}\Documentation

4 Application

Configure a Home instruction for Handler that is using a CIP Drive as Physical Axis.

- Home Instruction is a Method for the Handler
 - Handle Tag Name = ram_LD_AxisHandler_CD_Hndl
 - Path Axis (Virtual Axis) = Axis_PTH
 - Physical Axis (CIP Drive Axis) = Axis_PHY
 - In this example, the register Method ID is 1003
 - Handler ID = 1
 - Method is the 3rd to be register to this Handler



5 Appendix

General

This document provides a programmer with details on this instruction for a Logix-based controller, its Application Code Manager library content, and visualization content, if applicable. This document assumes that the programmer is already familiar with how the Logix-based controller stores and processes data.

Novice programmers should read all the details about an instruction before using the instruction. Experienced programmers can refer to the instruction information to verify details.

IMPORTANT

This object includes a Logix Designer Asset for use with Version 30 or later of Studio 5000 Logix Designer.

Common Information for All Instructions

Rockwell Automation Application Content may contain many common attributes or objects. Refer to the following reference materials for more information:

- Foundations of Modular Programming, **IA-RM001C-EN-P**

Conventions and Related Terms

Data - Set and Clear

This manual uses set and clear to define the status of bits (Booleans) and values (non-Booleans):

This Term:	Means:
Set	The bit is set to 1 (ON) A value is set to any non-zero number
Clear	The bit is cleared to 0 (OFF) All the bits in a value are cleared to 0

Signal Processing - Edge and Level

This manual uses Edge and Level to describe how bit (BOOL) Commands, Settings, Configurations and Inputs to this instruction are sent by other logic and processed by this instruction.

Send/Receive

Method: **Description:**

Edge

- Action is triggered by "rising edge" transition of input (0-1)
- Separate inputs are provided for complementary functions (such as "enable" and "disable")
- Sending logic SETS the bit (writes a 1) to initiate the action; this instruction CLEARS the bit (to 0) immediately, then acts on the request if possible
- LD: use conditioned OTL (Latch) to send
- ST: use conditional assignment [if (condition) then bit:=1;] to send
- FBD: OREF writes a 1 or 0 every scan, should use Level, not Edge

Edge triggering allows multiple senders per Command, Setting, Configuration or Input (many-to-one relationship)

Level

- Action ("enable") is triggered by input being at a level (in a state, usually 1)
- Opposite action ("disable") is triggered by input being in opposite state (0)
- Sending logic SETS the bit (writes a 1) or CLEARS the bit (writes a 0); this instruction does not change the bit
- LD: use OTE (Energize) to send
- ST: use unconditional assignment [bit:= expression_resulting_in_1_or_0;] or "if-then-else" logic [if (condition) then bit:= 1; else bit:= 0;]
- FBD: use OREF to the input bit

Level triggering allows only one sender can drive each Level

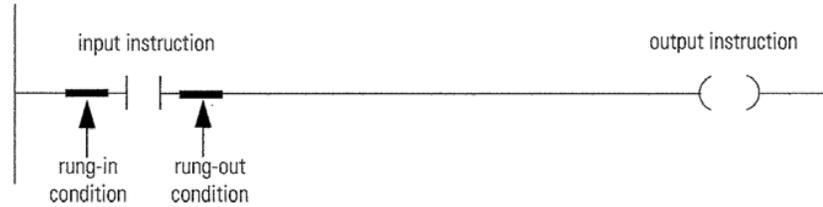
Instruction Execution - Edge and Continuous

This manual uses Edge and Continuous to describe how an instruction is designed to be executed.

Method:	Description:
Edge	<ul style="list-style-type: none">• Instruction Action is triggered by "rising edge" transition of the rung-in-condition
Continuous	<ul style="list-style-type: none">• Instruction Action is triggered by input being at a level (in a state, usually 1)• Opposite action is triggered by input being in opposite state (0)• Instructions designed for continuous execution should typically be used on rungs without input conditions present allowing the instruction to be continuously scanned

Relay Ladder Rung Condition

The controller evaluates ladder instructions based on the rung condition preceding the instruction (rung-in condition). Based on the rung-in condition and the instruction, the controller sets the rung condition following the instruction (rung-out condition), which in turn, affects any subsequent instruction.



If the rung-in condition to an input instruction is true, the controller evaluates the instruction and sets the rung-out condition based on the results of the instruction. If the instruction evaluates to true, the rung-out condition is true; if the instruction evaluates to false, the rung-out condition is false.

IMPORTANT

The rung-in condition is reflected in the EnableIn parameter and determines how the system performs each Add-On Instruction. If the EnableIn signal is TRUE, the system performs the instruction's main logic routine. Conversely, if the EnableIn signal is FALSE, the system performs the instruction's EnableInFalse routine.

The instruction's main logic routine sets/clears the EnableOut parameter, which then determines the rung-out condition. The EnableInFalse routine cannot set the EnableOut parameter. If the rung-in condition is FALSE, then the EnableOut parameter and the rung-out condition will also be FALSE.

Pre-scan

On transition into RUN, the controller performs a pre-scan before the first scan. Pre-scan is a special scan of all routines in the controller. The controller scans all main routines and subroutines during pre-scan, but ignores jumps that could skip the execution of instructions. The controller performs all FOR loops and subroutine calls. If a subroutine is called more than once, it is performed each time it is called. The controller uses pre-scan of relay ladder instructions to reset non-retentive I/O and internal values.

During pre-scan, input values are not current and outputs are not written. The following conditions generate pre-scan:

- Transition from Program to Run mode.
- Automatically enter Run mode from a power-up condition.

Pre-scan does not occur for a program when:

- Program becomes scheduled while the controller is running.
- Program is unscheduled when the controller enters Run mode.

IMPORTANT

The Pre-scan process performs the Process Add-On Instruction's logic routine as FALSE and then performs its Pre-scan routine as TRUE.
